

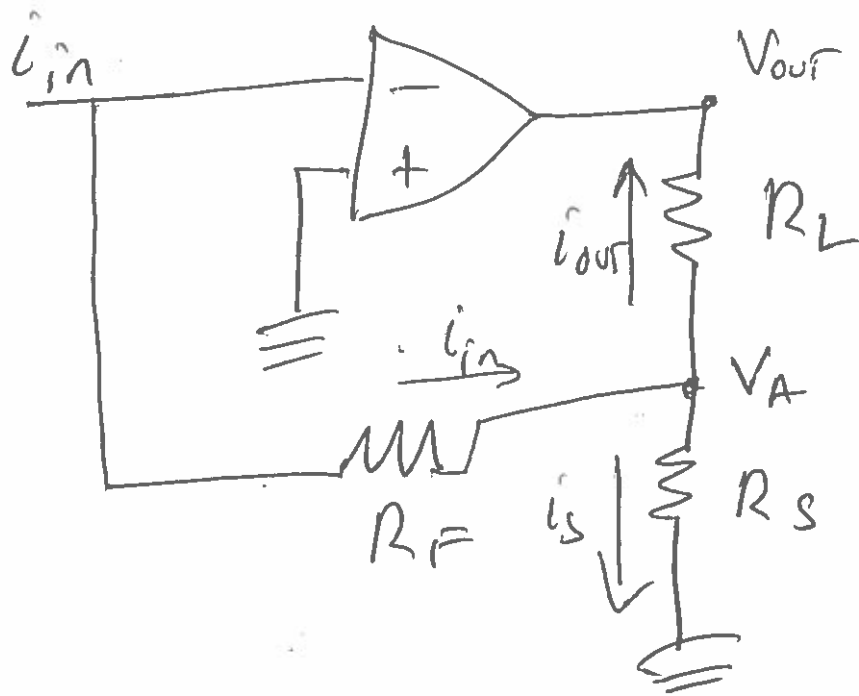
$$V_{out} = V_R - i_{in} R_F$$

CONVERTS CURRENT TO VOLTAGE

Let $i_{in} = \underline{1 \mu A}$ Use $R_F = 10 M\Omega$
 $V_R = 0$

$$V_{out} = -i_{in} R_F = -(1 \times 10^{-6}) (10 \times 10^6)$$

$$V_{out} = \underline{10 V}$$



$$i_{out} = i_{in} - i_s$$

$$i_{in} = \frac{0 - V_A}{R_F}$$

$$i_s = \frac{V_A}{R_S}$$

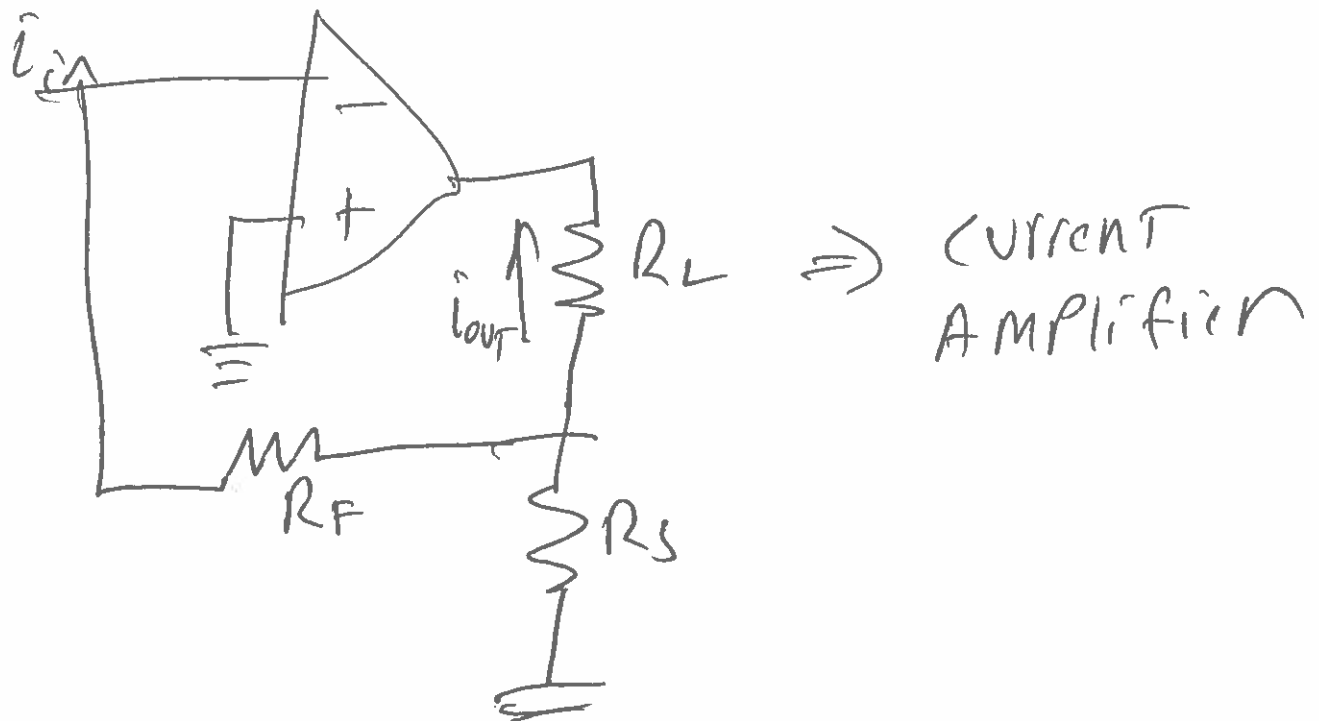
$$i_{out} = \frac{0 - V_A}{R_F} - \frac{V_A}{R_S} = -\frac{V_A}{R_F} \left(1 + \frac{R_F}{R_S} \right)$$

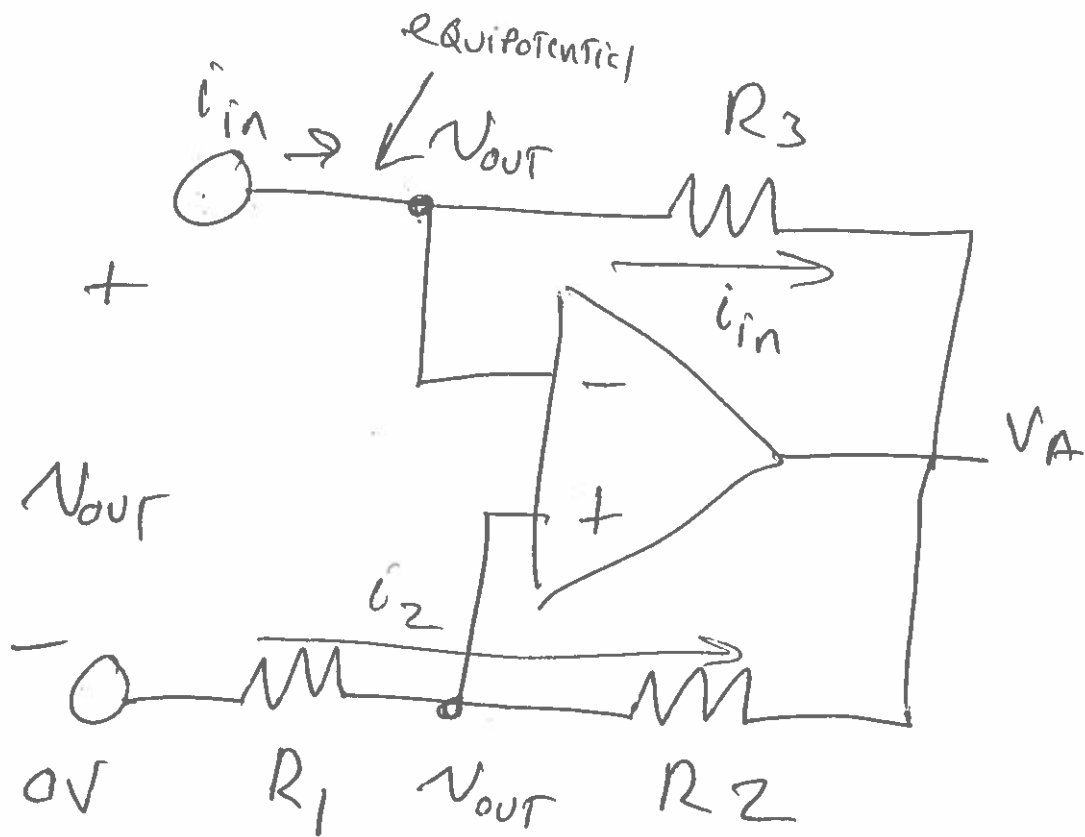
~~i_{OUT}~~ \Rightarrow (\ominus)

$$U_{Sins} \quad v_{in} = -\frac{V_A}{R_F}$$

$$i_{OUT} = v_{in} \left(1 + \frac{R_F}{R_S} \right)$$

$$\frac{i_{OUT}}{i_{in}} = 1 + \frac{R_F}{R_S} = \text{CURRENT GAIN}$$

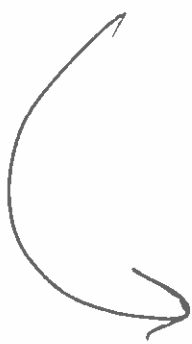




because $V_- = V_+$

$$i_{in} = \frac{V_{out} - V_A}{R_3}$$

$$i_2 = \frac{0 - V_{out}}{R_1} = \frac{V_{out} - V_A}{R_2}$$



$$-V_{out} = \frac{R_1}{R_2} V_{out} - \frac{R_1}{R_2} V_A$$

$$V_{out} \left(1 - \frac{R_1}{R_2}\right) = \frac{R_1}{R_2} V_A \quad \text{can do, not useful}$$

$$V_{OUT} - V_A = -V_{OUT} \frac{R_2}{R_1}$$

$$\hat{i}_{in} = \frac{V_{OUT} - V_A}{R_3} = \frac{-V_{OUT} R_2 / R_1}{R_3} = \frac{-V_{OUT} R_2}{R_1 R_3}$$

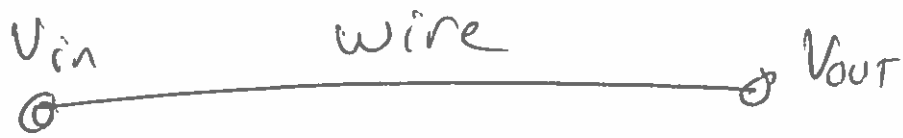
$$V_{OUT} = -\hat{i}_{in} \frac{R_1 R_3}{R_2}$$

$$\frac{V_{OUT}}{\hat{i}_{in}} = R_{eff} = -\frac{R_1 R_3}{R_2} \quad \text{Negative Resistance}$$

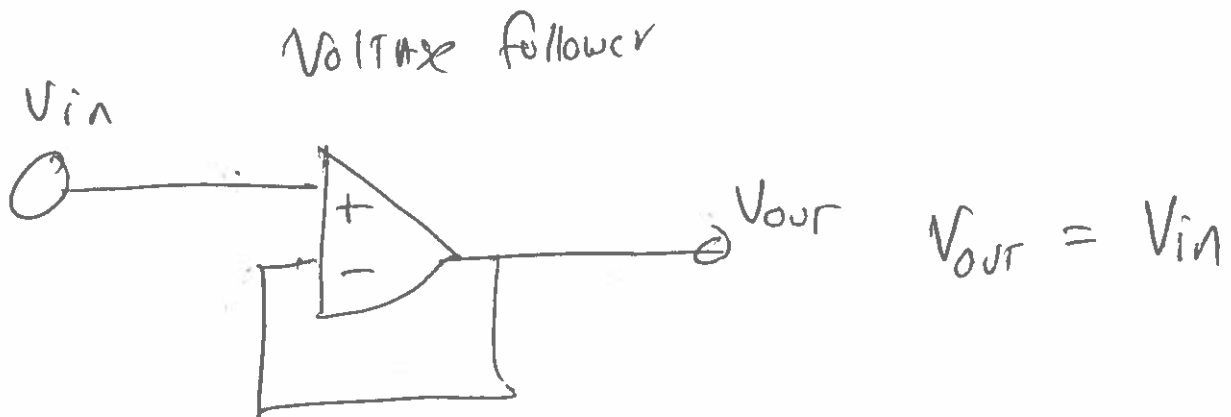
PASSIVE RESISTORS CAN NEVER BE NEGATIVE

ACTIVE RESISTOR (HAS SEPARATE POWER SOURCE)

THEFORE CAN GET QS INCREASING CURRENT
RATHER THAN DECREASING CURRENT.



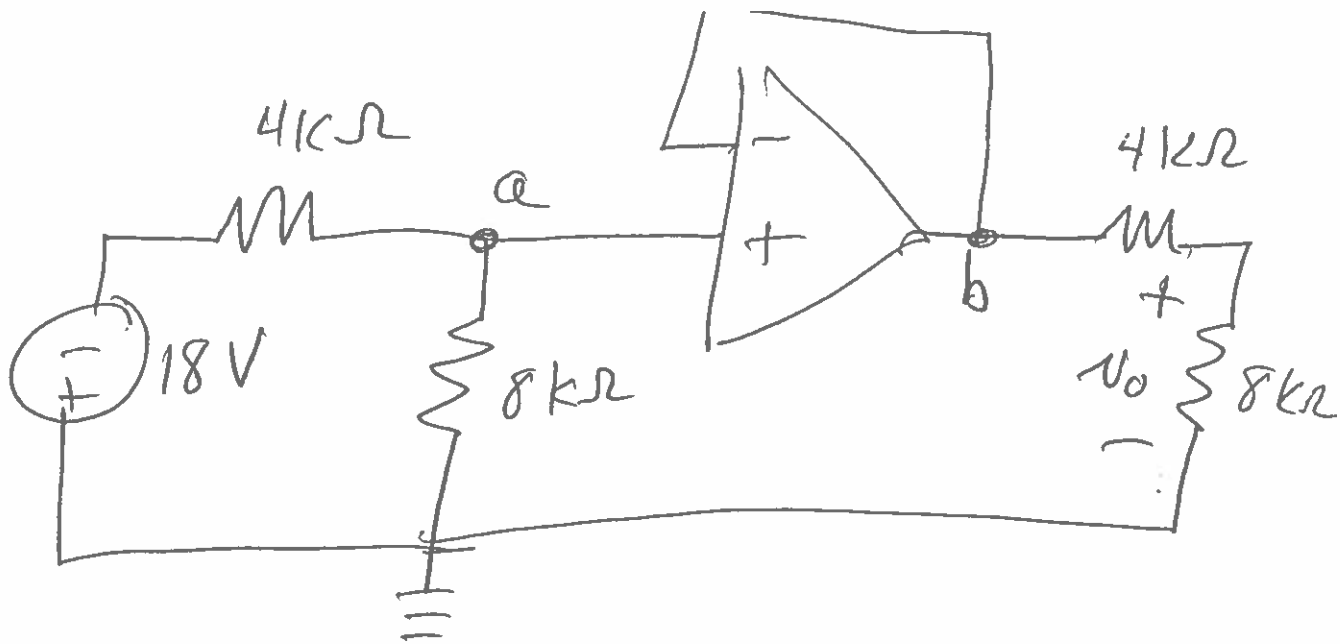
$$V_{out} = V_{in}$$



This draws more energy

USE VOLTAGE follower TO PROTECT @

by limiting VOLTAGE Allowed
limiting CURRENT Allowed



$$V_{out} = ? \quad V_{out} = V_b \frac{8k\Omega}{4k\Omega + 8k\Omega}$$

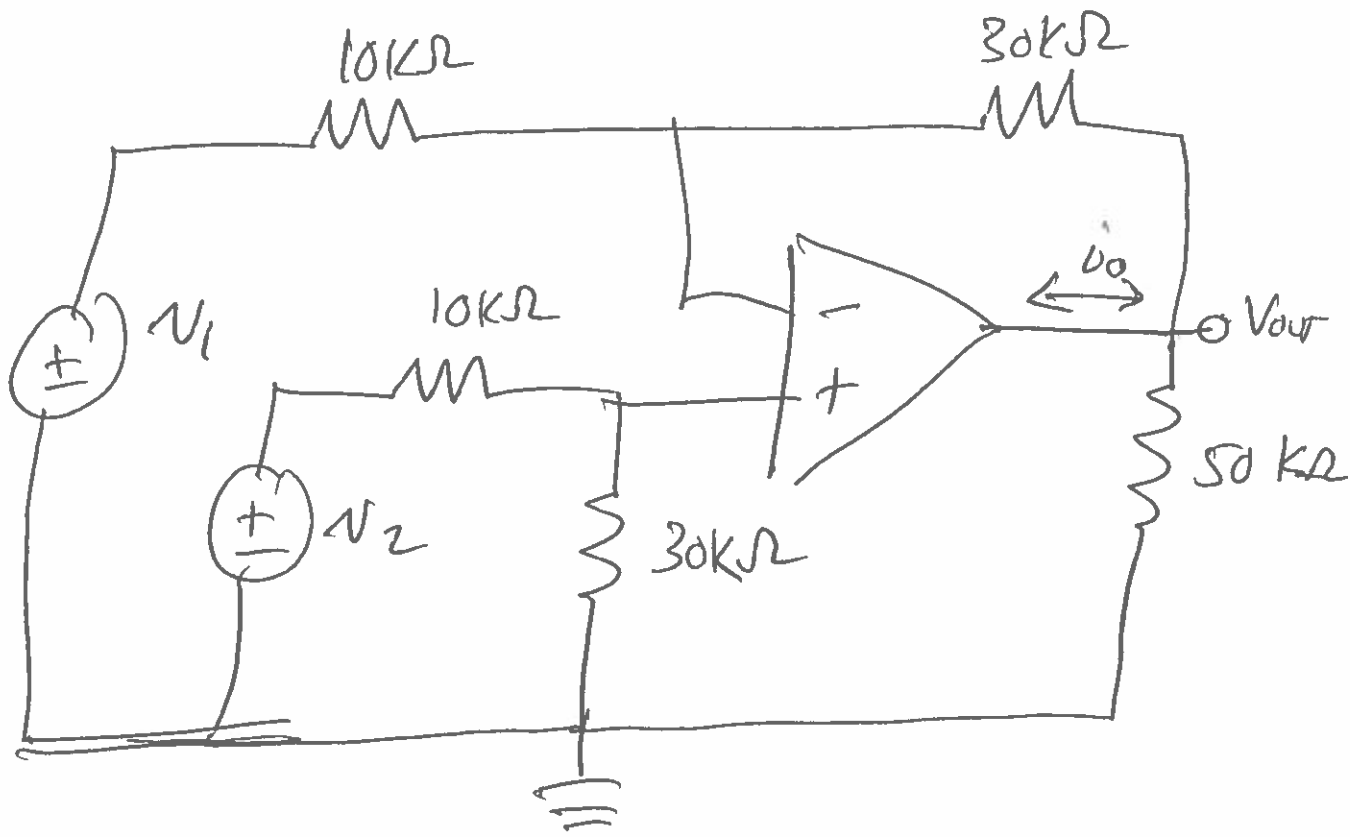
$$V_a = \frac{(-18V) 8k\Omega}{4k\Omega + 8k\Omega} = \left(-\frac{3}{2}\right) \frac{8}{12} \times 4$$

$$V_a = -12V$$

$$V_b = V_a \quad \text{So } V_{out} = (-12V) \frac{8}{12}$$

$$V_{out} = -8V$$

Node Analysis with OP-AMPS



OP-AMP is either a current source or sink to V_{out} . So you cannot write V_{out} as a primary node equation.